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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/761,292	01/22/2004	David Y. Chan	0267-001-2035	3157	
31108	7590 04/21/2006		EXAM	EXAMINER	
PAUL J. SU	JTTON, ESQ., BARR	BENENSO	BENENSON, BORIS		
GREENBER	G TRAURIG, LLP	•			
200 PARK AVENUE			ART UNIT	PAPER NUMBER	
NEW YORK	C. NY 10166		2836	<u> </u>	

DATE MAILED: 04/21/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	Applicant(s)	100
Office Action Commence		10/761,292	CHAN ET AL.	•
	Office Action Summary	Examiner	Art Unit	
		Boris Benenson	2836	
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the c	orrespondence addr	'ess
WHIC - External after - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DANSIONS OF THE MAILING THE MAIL	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this com D (35 U.S.C. § 133).	
Status				
· ·	Responsive to communication(s) filed on <u>22 Ja</u> This action is <b>FINAL</b> . 2b) This Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro		nerits is
Dispositi	ion of Claims			
5)□ 6)⊠ 7)□	Claim(s) <u>1-9</u> is/are pending in the application.  4a) Of the above claim(s) is/are withdray Claim(s) is/are allowed.  Claim(s) <u>1 and 2</u> is/are rejected.  Claim(s) <u>3-9</u> is/are objected to.  Claim(s) are subject to restriction and/or			
Applicati	ion Papers			
10)⊠	The specification is objected to by the Examine The drawing(s) filed on 22 January 2004 is/are: Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction to the oath or declaration is objected to by the Example.	a)⊠ accepted or b)⊡ objected drawing(s) be held in abeyance. See ion is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR	? 1.121(d).
Priority u	under 35 U.S.C. § 119			
12) a)[	Acknowledgment is made of a claim for foreign  All b) Some * c) None of:  1. Certified copies of the priority documents  2. Certified copies of the priority documents  3. Copies of the certified copies of the priority application from the International Bureau  See the attached detailed Office action for a list of	s have been received. s have been received in Applicati ity documents have been receive ı (PCT Rule 17.2(a)).	on No ed in this National St	tage
2) 🔲 Notic 3) 🔲 Inforr	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:		52)

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#### Detailed Actions

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 1. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Finlay et al. (6,522,510). Finlay et al. disclose a device a Ground Fault Circuit Interrupter With Miswire Protection And Indication. The interrupter includes a detection circuit, a pair of load terminals and a pair of line terminals and comprises a current limiting circuit (Fig.1, Pos. R13, R10) coupled to at least one of line terminals to pass a current for a defined interval of time and sufficient magnitude to trip the circuit interrupter when the device is reverse wired. Finlay et al. disclose "The estimated time it takes for the fault resistors R10, R13 to "clear" or bum out is greater than 50 ms. Because the trip time of the GFCI is less than or equal to 25

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ms, fault resistors R10, R13 do not have enough time to clear. If one attempts to reset the device when in the miswired condition, the device immediately trips out again, and this continues until such time as the device is wired correctly" (Col. 4, Lines 7-14).

# Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Finlay et al. (6,522,510). Finlay et al. disclose all the limitation of Claim 1, as it was discussed above. Finlay et al. did not disclose the device wherein the current limiting circuit is coupled between the terminal to be connected to the phase conductor and a ground terminal. Finlay et al. disclose the device wherein the current limiting circuit is coupled between the terminal to be connected to the phase

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conductor and a neutral terminal. It is well known that to have a wiring system with grounded neutral line as it is done in some European countries. It would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified Finlay et al. and connect the current limiting circuit is coupled between the terminal to be connected to the phase conductor and the ground terminal, because it will allow use of the device in two-wire power distribution environment.

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### Allowable Subject Matter

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3. Claims 2-9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

# The following is an examiner's statement of reasons for allowance:

4. Claims 3-9 are allowable because none of the prior art of record disclose a circuit-interrupting device with miswiring protection wherein a current limiting circuit that passes a current sufficient to trip the circuit interrupting device when

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the device is reverse wired comprises a diode in series with a resistor and a capacitor.

#### Contact information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Boris Benenson whose telephone number is (571) 272-2048. The examiner can normally be reached on M-F (8:20-6:00) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on (571) 272-2800 ext 36. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Boris Benenson

Examiner

BRIAN SIRCUS SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800